

**PATENT APPLICATION**

Sheet 1 of 1

<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	<b>200315309-1</b>		
	APPLICANT		
	<b>Karl Joseph Bois et al.</b>		
	FILING DATE	GROUP	
	<b>02/19/04</b>	<b>2841</b>	

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
TD	1A	4,747,897	05-31-1988	Johnson	
TD	1B	6,353,997	03-12-2002	Su	
TD	1C	6,387,205	05-14-2002	Appelt et al.	
TD	1D	2002/0125044	09-12-2002	Johnson	
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

TD	1Q	* Printed Wiring Board Products*; Hitachi Chemical Co. America, Ltd.; 2 pages; undated.
TD	1R	PARTANGEL, NARAYANAN S.; Master's Thesis; * The Relationship Between Process and Manufacturing Plant Performance: A Goal Programming Data Envelopment Analysis Approach*; Chapter 5; Digital Library and Archives; Pages 47-56; September 17, 1999
TD	1S	* A Low Loss Dielectric for High Frequency HDI Substrates and PCBs*; Microwave Journal; 2 pages; December 2000

EXAMINER /Tuan Dinh/

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